



DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
INSTITUTE OF TECHNOLOGY
UNIVERSITY OF KASHMIR
NAAC Accredited Grade – “A+”

F-(Timetable-ECE-3rd) AUT-DECE/IOT/KU/23 /01
Dated:21/08/2023

TIME TABLE B. TECH 3rd SEMESTER

Semester Incharge: Er. Rouf-Ul-Alam Bhat and
Dr. Mir Mohsina

W.E.F. 22/08/2023

Day	09:30 am-10:30 am	10:30 am-11:30 am	11:30 am-12:30 pm	12:30 pm-01:30 pm	01:30 pm-02:30 pm	02:30 pm-03:00 pm	03:00 pm-05:00 pm
Monday	DS	AE-I	MTH-III	DSD-I	NAMAZ/ BREAK	Doubt Clearance Session/Extra Curricular Activities	DSD-I LAB (G1)/NAS LAB (G2)
Tuesday	NAS	DSD-I	DS	DS			EDAT-I LAB (G2)/DS LAB (G1)
Wednesday	NAS	MTH-III	AE-I	ECA			AE-I LAB (G2)/EDAT-I LAB (G1)
Thursday	NAS	NAS	AE-I	LIBRARY			DS LAB (G2)/NAS LAB (G1)
Friday	DSD-I	DSD-I	MTH-III	LIBRARY			DSD-I LAB (G2)/AE-I LAB (G1)

THEORY

S.No.	COURSE CODE	COURSE TITLE	SHORT FORM	FACULTY	VENUE	HRS./WK
1.	BSCMTH31	ENGINEERING MATHEMATICS-III	MTH-III	Dr. IRFAN FAIQ	ROOM NO, A-203 2nd FLOOR BLOCK A	3
2.	PCCECE32	NETWORK ANALYSIS AND SYNTHESIS	NAS	Er. SHAZIA RASHID		4
3.	PCCECE33	ANALOG ELECTRONICS CIRCUITS I	AE-I	Dr. MIR MOHSINA		3
4.	PCCECE34	DIGITAL SYSTEM DESIGN I	DSD-I	Er. ROUF UL ALAM		4
5.	ESCECE35	DATA STRUCTURES	DS	Dr. MANASHA SAQIB		3

LAB

S.NO.	COURSE CODE	COURSE TITLE	SHORT FORM	DEMONSTRATOR	SUPPT. FACULTY	VENUE	HRS./WK
1.	PCCECE32L	NETWORK ANALYSIS AND SYNTHESIS LAB	NAS LAB	Er. SHAZIA RASHID	-	PHYSICS LAB BLOCK-B	4
2.	PCCECE34L	DIGITAL SYSTEM DESIGN-I LAB	DSD-I LAB	Er. ROUF UL ALAM	-	PROJECT LAB BLOCK B	4
3.	PCCECE33L	ANALOG ELECTRONICS CIRCUITS-I LAB	AE-I LAB	Dr. MIR MOHSINA	-	L1 BLOCK-A 2 nd FLOOR	4
4.	ESCECE35L	DATA STRUCTURES	DS-LAB	Dr. MANASHA SAQIB	-	COMPUTATION LAB BLOCK B	4
5.	PCCECE36L	EDA-TOOLS LAB-I	EDAT-I LAB	Dr. BILAL A. MALIK	-	PROJECT LAB BLOCK B	4

Sd/-
Faculty Asst.TT

Sd/-
I/C, TT, ECE

Sd/-
Ad I/C, ECE

Sd/-
Coordinator, ECE