



**Department of ECE
Institute of Technology,
University of Kashmir,
Zakura Campus, Srinagar**

No: F(datesheet/mid term/6th 8th sem/DoECE)IoT/KU/24,
Dated: 24 July, 2024

Revised Mid-Term (theory and Lab) examination schedule for B.Tech 6th and 8th semester of ECE (Regular/ backlog) is as per following:

TIME: Theory: 10:00:AM to 11:30AM

Lab: 12:00PM onwards

Date	Theory		Lab
	VENUE: Lecture Hall 1 and 2, 2nd floor, BLOCK A		VENUE: Respective labs
8/5/2024	Electrical Machines//COA	Wireless Communication	Wireless Communication lab
8/6/2024	Microcontrollers and Embedded system	****	Microcontrollers and Embedded system lab
8/7/2024	EMI/ power Electronics	computer network and security	EMI lab/computer network and security lab
8/8/2024	Communication System II	****	CS II LAB
8/9/2024	VLSI design	Organisation of Engineering system and HR management	VLSI DESIGN LAB
8/12/2024	Advanced Embedded system Prototyping / Mathematics for Machine learning	IOT/ deep learning	IOT/ Deep learning Lab

**Sd/-
Coordinator
Department of Electronics and Communication Engineering**

Copy to:

1. Dean, School of Engineering
2. Director, Institute of Technology
3. Head/ Coordinators, Institute of Technology
4. Concerned Faculty Members
5. File