



DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING Institute of Technology, University of Kashmir

TIME TABLE –B-Tech 7th SEMESTER

Semester I/C: Er. Rouf ul Alam Bhat

W.e.f: 20th August 2026

DAY	09:30 AM-10:30 AM	10:30 AM-11:30 AM	11:30 AM-12:30 PM	12:30 PM-01:30 PM	01:30PM-02:00 PM	02:00 PM-03:00 PM	03:00 PM-04:00 PM	04:00 PM-05:00 PM
Monday	PE	COA	IML/OCS	BREAK	ECA	ME LAB (G1)/)/ PP-I LAB(G2)		
Tuesday	DCOM	IML/OCS	ME	BREAK	ECA	LIBRARY	IML/OCS LAB	
Wednesday	ME	PE	COA	BREAK	ECA	PP-I LAB(G1)/ DCOM LAB(G2)		
Thursday	COA	DCOM	ME	BREAK	ECA	PE LAB(G1)/ ME LAB(G2)		
Friday	PE	IML /OCS	DCOM	BREAK	ECA	DCOM LAB(G1)/ PE LAB (G2)		

COURSECODE	COURSETITLE	SHORT FORM	FACULTY	VENUE	CREDITS
ESCECE71	Power Electronics	PE	Dr. Zahid (DoEE)	Lecture Hall 2 2nd Floor Block A	3
PCCECE72	Data Communication	D.COM	Er. Rouf Ul Alam/Er. Ayesha		3
PCCECE73	Microwave Engineering	ME	Dr. Injila		4
PCCECE74	Computer Organisation & Architecture	COA	Dr. Shaiqa		3
PEC2ECE705E	Optical Communication Systems	OCS	Dr. Ifat Rasheed		3
PEC2ECE701E	Introduction to Machine Learning	IML	Dr. Bilal Ahmad Malik	Block C	
ESCECE71L	Power Electronics Lab	PE Lab	Dr. Zahid (DoEE)	PE Lab, Block A, Ist Floor	1
PCCECE72L	Data Communication Lab	DCOM Lab	Er. Rouf ul Alam/Er. Ayesha	L4-Block –A, 2nd Floor	1
PCCECE73L	Microwave Engineering Lab	ME Lab	Dr. Injila	L4-Block –A 2nd Floor	1
PEC2ECE706E	Optical Communication Systems Lab	OCS Lab	Dr. Ifat Rasheed	L4-Block –A, 2nd Floor	1
PEC2ECE702E	Introduction to Machine Learning Lab	IML LAB	Dr. Bilal Ahmad Malik	Project Lab, Block B	1
PSIECE76	Project (Phase 1)	PP-I	Concerned Supervisors	Project Lab, Block B	1

Sd/-
Faculty Asst. I/C–TT,ECE

Sd/-
Coordinator, ECE