



DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING
INSTITUTE OF TECHNOLOGY
UNIVERSITY OF KASHMIR
NAAC Accredited Grade –“A+”

TIMETABLE – M.TECH 1st SEMESTER
 Semester I/C: Dr. Bilal A. Malik & Dr. Bazila
 W.e.f: 15th April 2024

DAY	09:30 AM - 10:30 PM	10:30 AM – 11:30 PM	11:30 AM – 12:00 PM	12:00 PM- 01:00 PM	01:00 PM - 02:00 PM	02:00 PM- 03:00 PM	03:00PM- 04:00PM	04:00 PM- 05:00 PM
Monday	SEMINAR	ADSP LAB		BREAK	DIP	AICD	LIBRARY	
Tuesday	CIIE				AICD	AICD	ADSP	ADSP
Wednesday	DIP LAB				DIP	DIP	ADSP	ECA
Thursday	ACST		SEMINAR		ECA	AICD	RML	RML
Friday	ACST		LIBRARY		ADSP	DIP	RML	RML

COURSE CODE	COURSE TITLE	SHORT FORM	FACULTY	VENUE	HRS./WK
MECE-101C	Analog Integrated Circuit Design	AICD	Dr. Zaid Shah	BLOCK-A, 2 nd FLOOR, MCL-1	4
MECE-102C	Advanced DSP	ADSP	Dr. Bazila	BLOCK-A, 2 nd FLOOR, MCL-1	4
MECE-103CL	Advanced Computation & Simulation Tools	ACST (MATLAB)	Dr. Mohammad Ayoub Sofi	BLOCK-A, 2 nd FLOOR,L2	4.5
		ACST (OrCAD)		BLOCK-B, 2 nd FLOOR PROJECT LAB	
		ACST (PYTHON)		BLOCK-B, 2 nd FLOOR PROJECT LAB	
PEC1MECE106	Digital Image Processing	DIP	Dr. Bazila	BLOCK-A, 2 nd FLOOR, MCL-1	4
PEC1MECE106 L	Digital Image Processing	DIP LAB	Dr. Bazila	BLOCK-A, 2 nd FLOOR, L2	2.5
MECE-104C	Research Methodology & Language	RML	Dr. Bilal A. Malik	BLOCK-A, 2 nd FLOOR, MCL-1	4
MECE-102CL	Advanced DSP Lab	ADSP LAB	Dr. Bazila	BLOCK-A, 2 nd FLOOR, L2	2.5
---	Centre for Innovations ,Incubation and Entrepreneurship	CIIE	CIIE STAFF	BLOCK-A, 2 nd FLOOR, MCL-1	2.5
---	Extracurricular Activities	ECA	--	--	--

Sd/-
I/C – TT, ECE

Sd/-
Coordinator, ECE