



**DEPARTMENT OF ELECTRONICS & COMMUNICATION ENGINEERING**  
**INSTITUTE OF TECHNOLOGY**  
**UNIVERSITY OF KASHMIR**  
 NAAC Accredited Grade –“A+”

**TIMETABLE – B.TECH 6<sup>th</sup> SEMESTER**  
 Semester I/C: Er. Rouf ul Alam Bhat & Dr. Bazila  
 W.e.f: 01<sup>st</sup> April 2025

DAY	9:30 AM – 10:15 AM	10:15 AM- 11:00 AM	11:00 AM - 11:45 AM	11:45 AM - 12:30 PM	12:30 PM - 1:15 PM	01:15 PM- 2:00 PM	02:00 PM - 2:45 PM	2:45 PM – 3:30 PM	03:30 PM - 04:15 PM	04:15 PM- 05:00 PM
Monday	VLSI	EMI	EM	ECA	MCES	<b>BREAK</b>	CS-II	EMI LAB(G1)/ CS-II LAB(G2)/		
Tuesday	MCES	EM	CS-II	EMI	ECA		VLSI	SEMINAR(G2)/ CS-II LAB(G1)		
Wednesday	CS-II		MCES	VLSI			SSD/MML	SEMINAR (G1)/ MCES LAB (G2)		
Thursday	SSD/MML		EM	CS-II	ECA		MCES	VLSI LAB(G1)/ EMI LAB(G2)		
Friday	EMI		SSD/MML	EM	ECA		ECA	MCES LAB(G1)/ VLSI LAB(G2)		

COURSE CODE	COURSE TITLE	SHORT FORM	FACULTY	VENUE	HRS./WK
PCCECE61	Communication Systems - II	CS-II	Dr. Injila	ROOM NO, A-210 2 <sup>ND</sup> FLOOR BLOCK A	4
PCCECE62	Microcontrollers and Embedded Systems	MCES	Dr. Shazia Ashraf		3
PCCECE63	Electrical Machines	EM	Dr. Zahid		3
PCCECE64	Electronic Measurement & Instrumentation	EMI	Dr. Bazila		3
PCCECE65	VLSI Design	VLSI	Dr. Saima		3
PEC1ECE601E/ PEC1ECE606E	Solid-State Devices/ Mathematics for Machine Learning	SSD/ MML	Dr. Hameem Habib / Dr. Bilal A. Malik		3
PCCECE61L	Communication Systems Lab - II	CS-II LAB	Dr. Injila	L4, Block A - 1 <sup>st</sup> Floor	5
PCCECE62L	Microcontrollers and Embedded Systems Lab	MCES LAB	Dr. Shazia Ashraf	L2, Block A - 1 <sup>st</sup> Floor	5
PCCECE64L	Electronic Measurement & Instrumentation Lab	EMI LAB	Dr. Bazila	L3, Block A - 1 <sup>st</sup> Floor	5
PCCECE65L	VLSI Design Lab	VLSI LAB	Dr. Saima	Project lab, Block B	5
PSIECE66	Seminar	Seminar	Dr. Omar	-	6

Sd/-  
I/C – TT, ECE

Sd/-  
Coordinator